

In re Application of:
Shiv Kumar Gupta

Serial No. 10/685,762

Filed: October 15, 2003

Group Art Unit: 2128

For: PARALLEL INSTANCES OF A PLURALITY OF SYSTEMS ON CHIP IN HARDWARE EMULATOR VERIFICATION

EXAMINER: GEBRESILASSIE, KIBROM K

CONFIRMATION NO. 6313

Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This paper responds to the Office Action dated May 27, 2009, in the above-identified application. The Applicant respectfully requests consideration of the following remarks.